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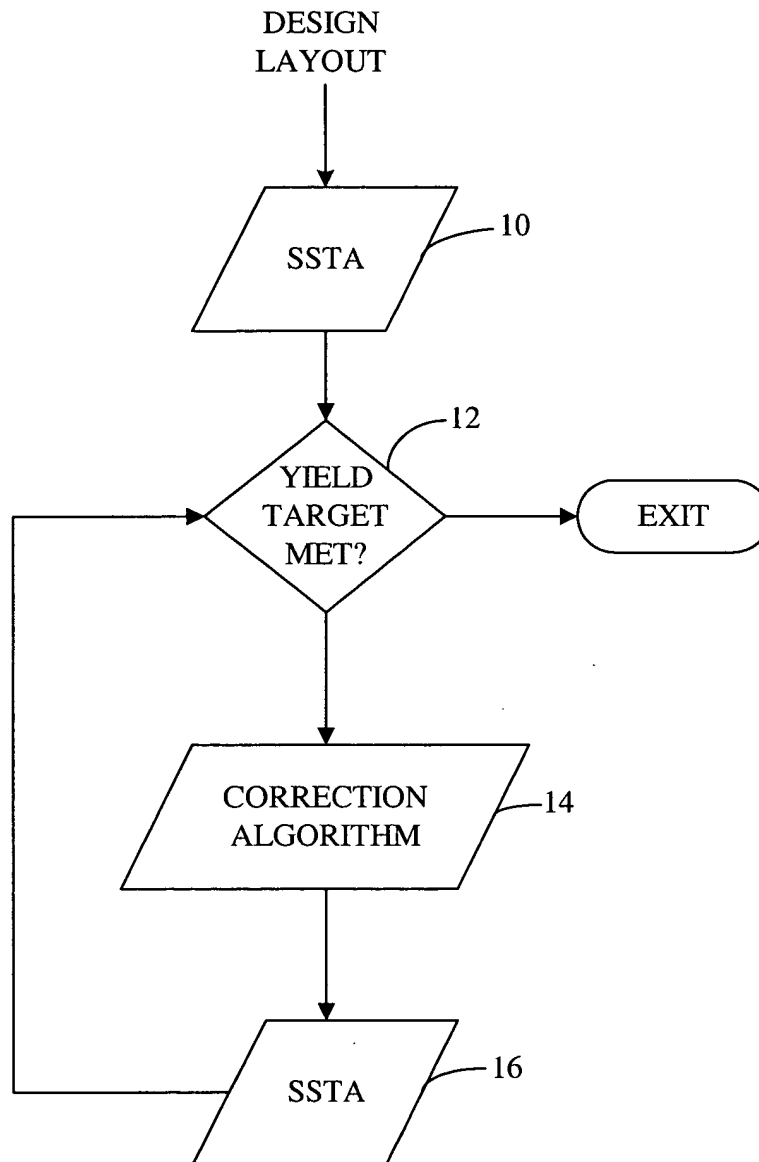


FIG. 1

02/25/04

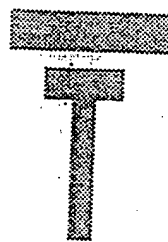


FIG. 2(a)

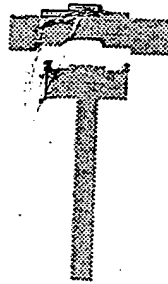


FIG. 2(b)

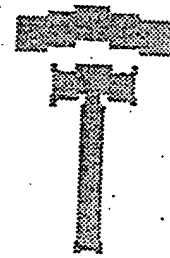


FIG. 2(c)

GATE SIZING PROBLEM	MINIMUM COST OF CORRECTION PROBLEM
AREA	COST OF CORRECTION
NOMINAL DELAY	DELAY $\mu + k\sigma$
CYCLE TIME	SELLING POINT DELAY
DIE AREA	TOTAL COST OF RET

FIG. 3